

# Brian J Gravelle

Curriculum Vitae

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## Education

Current            **University of Oregon PhD student**

2015              **B.S. Computer Engineering, Gonzaga University**

## Research Experience

2016 - Present    Graduate Research Fellow  
High-Performance Computing Laboratory  
Dept. of Computer and Information Science, University of Oregon  
PI: Dr. Boyana Norris  
Projects: Optimization of code for diverse computing environments

2015 - 2016      Graduate Research Fellow  
Computer Architecture and Embedded Systems Lab (CAES)  
Dept. of Computer and Information Science, University of Oregon  
PI: Dr. Michel A. Kinsky  
Projects: Sphinx secure hardware; DuckSim SoC simulation

2013 - 2015      Undergraduate Research Assistant  
Smart Antenna and Radio Lab (SARL)  
Dept. of Electrical and Computer Engineering, Gonzaga University  
PI: Dr. Steve Schennum  
Projects: helical multi-polarization antenna

## Teaching Experience

- Fall 2017            CIS 211 Computer Science II, Graduate Teaching Fellow  
Dept. of Computer and Information Science, University of Oregon
- Winter 2017        CIS 210 Computer Science I, Graduate Teaching Fellow  
Dept. of Computer and Information Science, University of Oregon
- Fall 2016            CIS 210 Computer Science I, Graduate Teaching Fellow  
Dept. of Computer and Information Science, University of Oregon
- Fall 2015            CIS 314 Computer Organization, Graduate Teaching Fellow  
Dept. of Computer and Information Science, University of Oregon

## Publications

- [1] Steven Schennum, Brian Gravelle, Caitlin Croskrey, James Smock, and Robert Conley. Dual feed omnidirectional antenna for adaptive polarization and mimo transceivers. In *Proceedings of Wireless Innovation Conference on Wireless Communications Technologies and Software Defined Radio*, pages 102–106, 2015.

## Posters

- [1] S. Khadka, S. Ergullu-Koehnen, B. Gravelle, and M. Kinsky. Neural network based predictive routing for network-on-chip architectures. In *Work-in-Progress Presentation at 53rd Design Automation Conference (DAC 2016)*, Austin, Texas, Jun. 5-9 2016.
- [2] P. Ren, M. Kinsky, C. Yang, B. Gravelle, S. Khadka, and N. Zheng. Copal: Connectivity preserving algorithm for network-on-chip power-gating. In *Work-in-Progress Presentation at 53rd Design Automation Conference (DAC 2016)*, Austin, Texas, Jun. 5-9 2016.

## Awards and Honors

Tau Beta Pi Engineering Honors Society  
Alpha Sigma Nu Honor Society  
2016 A. Richard Newton Young Student Fellow

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